

Amendments to the Specification

Please amend paragraph [0002] as follows:

This application is related to concurrently filed and commonly assigned U.S. patent application No. 10/759,988 ~~(Attorney Docket No. MIC-M097)~~, entitled “Numerical Value Conversion Using A Saturation Limited Arithmetic Logic Unit Supporting Variable Resolution Operands,” of the same inventors hereof.

Please amend paragraph [0004] as follows:

The present invention concerns devices that are used to convert numerical values from one unit system to another unit system. For example, an analog-to-digital convert (ADC) is often used to digitize an analog data source into digital values. The digital values, sometimes referred to as “digital bits,” often have to be expressed as ~~real-word~~ real-world parameters, such as voltage, current, and temperature. In operation, the ADC digitizes the analog data source (temperature, voltage or current) into digital values in arbitrary units. Then, the ADC uses an arithmetic logic unit (ALU) to convert the digital values in an arbitrary unit into an appropriate real-world unit (e.g., degree Celsius, volts and ampere). For example, the ADC may digitize an input voltage value and provide values in whole numbers of millivolts as the digital output.